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Tel: 408.382.0480
Fax: 408.382.0481**FACSIMILE COVER SHEET**

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Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

From:	Carmen C. Cook
Serial No.:	10/618,376
Docket:	MIC-M074
Re:	Response to Restriction Requirement
Pages:	2 (including cover sheet)

Message:

Re: Applicant(s): Shekar Mallikarjunaswamy
Assignee: Micrel, Inc.
Title: Lateral DMOS Transistor With A Self-Aligned Drain
Region
Serial No.: 10/618,376
Examiner: Steven Ho Yin Loke
Docket No.: MIC-M074

Filed: July 11, 2003
Group Art Unit: 2811

Dear Sir:

Transmitted herewith are the following documents in the above-identified application:

- (1) Response to Restriction Requirement (1 page).

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Shekar Mallikarjunaswamy
Assignee: Micrel, Inc.
Title: Lateral DMOS Transistor With A Self-Aligned Drain Region
Serial No.: 10/618,376 Filing Date: July 11, 2003
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San Jose, California
August 11, 2004

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Response To Restriction Requirement

Dear Sir:

In response to the Office Action mailed August 5, 2004, imposing a restriction requirement, Applicant hereby elects the first species (Figure 2) which is readable on claims 1-5, 10-13, 15-18 and 21-25, of which claim 1 is generic, for further prosecution in the above patent application. Accordingly, claims 6-9, 14, 19, 20 and 26-36 are withdrawn from consideration.

Should the Examiner have any questions, the Examiner is invited to call the undersigned at (408) 382-0480 extension 208.

Certification of Facsimile Transmission

I hereby certify that this paper is being facsimile transmitted to the U.S. Patent and Trademark Office on the date shown below.

Carmen C. Cook 8/11/2004
Signature Date

Respectfully submitted,

Carmen C. Cook

Carmen C. Cook
Attorney for Applicant(s)
Reg. No. 42,433

PATENT LAW
GROUP LLP
2635 N. FIRST ST.
SUITE 223
SAN JOSE, CA 95134
(408) 382-0480
FAX (408) 382-0481